

PATENT ABSTRACTS OF JAPAN

(11)Publication number : **56-146340**

(43)Date of publication of application : **13.11.1981**

(51)Int.Cl.

H04J 3/00
H04J 3/16

(21)Application number : **55-049481**

(71)Applicant : **NIPPON TELEGR & TELEPH CORP
<NTT>**

(22)Date of filing : **15.04.1980**

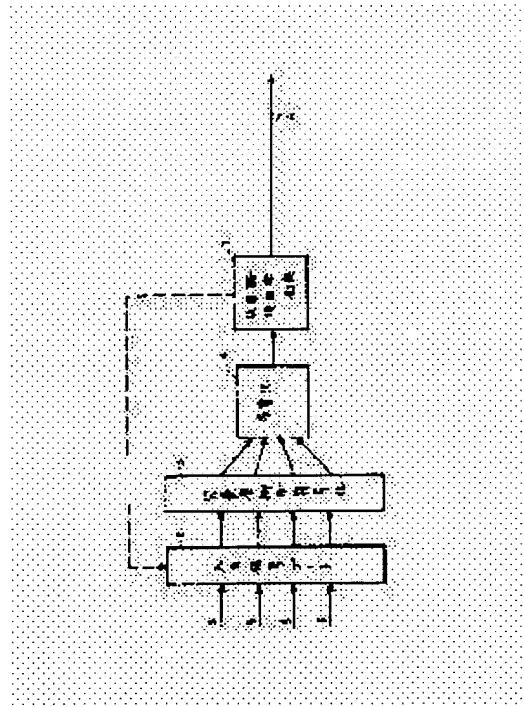
(72)Inventor : **YASUDA HIROSHI
HASHIMOTO HIDEO
KODERA HIROSHI**

(54) MULTIPLEX TRANSMITTING SYSTEM FOR REDUNDANCY SUPPRESSION CODING INFORMATION

(57)Abstract:

PURPOSE: To reduce the occurring probability for incapability of transmission, by designing previously the multiplex degree based on the average redundancy suppression factor and blocking the idle channel when the using efficiency of transmission line exceeds a certain level to inhibit the new input signal.

CONSTITUTION: Each signal supplied from the four input signal channels 5 is coded after receiving a suppression of redundancy through the redundancy suppressing and coding circuit 3. The multiplying circuit 4 multiplies the coded signal and then sends it onto the transmission line 6. In this case, the using efficiency measuring instrument 1 of transmission line monitors the transmission line using factor based on the information to be sent onto the line 4. Thus the input signal gate 2 is controlled.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office